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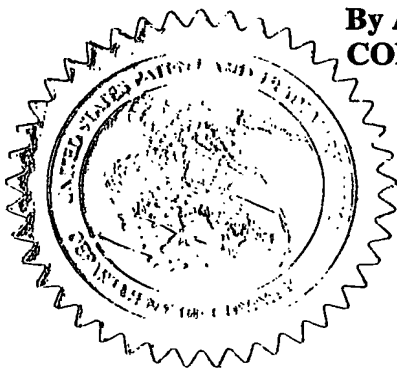
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PROVISIONAL APPLICATION COVER SHEET

This is a request for filing a PROVISIONAL APPLICATION under 37 CFR 1.53(c).

Docket Number 233234US6YA PROV

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TITLE OF THE INVENTION (280 CHARACTERS MAX)

METHOD AND APPARATUS FOR REDUCING SUBSTRATE BACKSIDE DEPOSITION DURING PROCESSING

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 ☐ CD(s), Number
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METHOD OF PAYMENT

- ☐ Applicant claims small entity status. See 37 CFR 1.27.
- ☒ A check or money order is enclosed to cover the Provisional Filing Fees
- ☐ The Commissioner is hereby authorized to charge filing fees and credit any overpayment to Deposit Account Number 15-0030

 PROVISIONAL
FILING FEE \$160.00
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The invention was made by an agency of the United States Government or under a contract with an agency of the United States Government.

- ☒ No.
- ☐ Yes, the name of the U.S. Government agency and the Government contract number are:

Respectfully Submitted,

3/21/2003

DATE

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PROVISIONAL APPLICATION FILING ONLY

APPLICATION DATA SHEET

APPLICATION INFORMATION

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| Title:: | METHOD AND APPARATUS FOR REDUCING SUBSTRATE BACKSIDE DEPOSITION DURING PROCESSING |
| Attorney Docket Number:: | 233234US6YAPROV |
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CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 22850

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Attorney Docket No. 233234US6YAPROV

**METHOD AND APPARATUS FOR REDUCING SUBSTRATE BACKSIDE
DEPOSITION DURING PROCESSING**

Field of the Invention

[0001] The present invention relates to a method and apparatus for reducing substrate backside deposition during processing and more particularly to a focus ring employed in a plasma processing system to surround a substrate.

Background of the Invention

[0002] The fabrication of integrated circuits (IC) in the semiconductor industry typically employs plasma to create and assist surface chemistry within a plasma reactor necessary to remove material from and deposit material to a substrate. In general, plasma is formed within the plasma reactor under vacuum conditions by heating electrons to energies sufficient to sustain ionizing collisions with a supplied process gas. Moreover, the heated electrons can have energy sufficient to sustain dissociative collisions and, therefore, a specific set of gases under predetermined conditions (e.g., chamber pressure, gas flow rate, etc.) are chosen to produce a population of charged species and chemically reactive species suitable to the particular process being performed within the chamber (e.g., etching processes where materials are removed from the substrate or deposition processes where materials are added to the substrate).

[0003] Although the formation of a population of charged species (ions, etc.) and chemically reactive species is necessary for performing the function of the plasma processing system (i.e. material etch, material deposition, etc.) at the substrate surface, other component surfaces on the interior of the processing chamber are exposed to the physically and chemically active plasma and, in time, can erode. The erosion of exposed components in the plasma processing system can lead to a gradual degradation of the plasma processing performance and ultimately to complete failure of the system.

[0004] In order to minimize the damage sustained by exposure to the processing plasma, a consumable or replaceable component, such as one

fabricated from silicon, quartz, alumina, carbon, or silicon carbide, can be inserted within the processing chamber to protect the surfaces of more valuable components that would impose greater costs during frequent replacement and/or to affect changes in the process. Furthermore, it is desirable to select surface materials that minimize the introduction of unwanted contaminants, impurities, etc. to the processing plasma and possibly to the devices formed on the substrate. Often times, these consumables or replaceable components are considered part of the process kit, which is frequently maintained during system cleaning. For example, a focus ring is generally employed to rest on the substrate holder and surround the substrate, wherein the presence of the focus ring affects the processing at the edge of the substrate.

Summary of the Invention

[0005] A method and apparatus for reducing substrate backside deposition during processing is described.

[0006] A focus ring assembly coupled to a substrate holder configured to support a substrate exposed to a process in a processing system comprising: a focus ring coupled to the substrate holder; and a secondary focus ring coupled to the focus ring and configured to reduce deposition of material from the process on a backside surface of the substrate.

[0007] A method of using a focus ring assembly for surrounding a substrate upon a substrate holder in a processing system comprising: installing the focus ring assembly in the processing system, wherein the focus ring assembly comprises a focus ring coupled to the substrate holder, and a secondary focus ring coupled to the focus ring and configured to reduce deposition of material from the process on a backside surface of the substrate; loading the substrate into the processing system; and processing the substrate.

Brief Description of the Drawings

[0008] These and other advantages of the invention will become more apparent and more readily appreciated from the following detailed description of the exemplary embodiments of the invention taken in conjunction with the accompanying drawings, where:

[0009] FIG. 1 illustrates a schematic block diagram of a plasma processing system according to an embodiment of the present invention;

[0010] FIG. 2 presents an expanded cross-sectional view of a portion of a conventional substrate holder in a plasma processing system as shown in FIG. 1;

[0011] FIG. 3 presents an expanded cross-sectional view of a portion of a substrate holder for a plasma processing system according to an embodiment of the present invention;

[0012] FIG. 4 presents an expanded cross-sectional view of a portion of a substrate holder for a plasma processing system according to an embodiment of the present invention;

[0013] FIG. 5 presents an expanded cross-sectional view of a portion of a substrate holder for a plasma processing system according to an embodiment of the present invention;

[0014] FIG. 6 presents an expanded cross-sectional view of a portion of a substrate holder for a plasma processing system according to an embodiment of the present invention;

[0015] FIG. 7 presents an expanded cross-sectional view of a portion of a substrate holder for a plasma processing system according to an embodiment of the present invention;

[0016] FIG. 8 presents an expanded cross-sectional view of a portion of a substrate holder for a plasma processing system according to an embodiment of the present invention; and

[0017] FIG. 9 presents a method of using a focus ring in a plasma processing system according to an embodiment of the present invention.

Detailed Description of an Embodiment

[0018] In plasma etching, a focus ring can be employed in order to affect the processing properties at the periphery of the substrate. For conventional systems, the focus ring, as described in greater detail below, simply rests atop the substrate holder adjacent the substrate, wherein its inner radius is slightly greater than the outer radius of the substrate. Typically, the focus ring comprises a material specifically chosen for the given process, such as silicon, quartz, alumina, sapphire, silicon carbide, carbon, etc.

[0019] For example, a plasma processing system 1, such as one capable of plasma etching, is depicted in FIG. 1 comprising a processing chamber 10, an upper assembly 20, an upper wall 24, a substrate holder 30 for supporting a substrate 35, and a pumping duct 40 coupled to a vacuum pump (not shown) for providing a reduced pressure atmosphere 11 in processing chamber 10. Processing chamber 10 can, for example, facilitate the formation of a processing plasma in a process space 12 adjacent substrate 35. The plasma processing system 1 can be configured to process various substrates (i.e. 200 mm substrates, 300 mm substrates, or larger).

[0020] In the illustrated embodiment, upper assembly 20 can comprise at least one of a cover, a gas injection assembly, and an upper electrode impedance match network. For example, the upper wall 24 can, for example, be configured to comprise an electrode that is coupled to a radio frequency (RF) source, and therefore facilitate an upper electrode for the plasma processing system 1. In another alternate embodiment, the upper assembly 20 comprises a cover and an upper wall 24, wherein the upper wall 24 is maintained at an electrical potential equivalent to that of the processing chamber 10. For example, the processing chamber 10, the upper assembly 20, and the upper wall 24 can be electrically connected to ground potential, and facilitate a grounded wall for the plasma processing system 1.

[0021] Processing chamber 10 can, for example, further comprise a deposition shield 14 for protecting the plasma processing chamber 10 from the processing plasma in the process space 12, and an optical viewport 16. Optical viewport 16 can comprise an optical window 17 coupled to the backside of an optical window deposition shield 18, and an optical window

flange 19 can be configured to couple optical window 17 to the optical window deposition shield 18. Sealing members, such as O-rings, can be provided between the optical window flange 19 and the optical window 17, between the optical window 17 and the optical window deposition shield 18, and between the optical window deposition shield 18 and the processing chamber 10.

Optical window deposition shield 18 can extend through an opening 70 within deposition shield 14. Optical viewport 16 can, for example, permit monitoring of optical emission from the processing plasma in process space 12.

[0022] Substrate holder 30 can, for example, further comprise a vertical translational device 50 surrounded by a bellows 52 coupled to the substrate holder 30 and the processing chamber 10, and configured to seal the vertical translational device 50 from the reduced pressure atmosphere 11 in processing chamber 10. Additionally, a bellows shield 54 can, for example, be coupled to the substrate holder 30 and configured to protect the bellows 52 from a processing plasma. Substrate holder 10 can further be coupled to a focus ring 60, and, optionally, a shield ring 56. Furthermore, a baffle plate 58 can, for example, extend about a periphery of the substrate holder 30. Baffle plate 58 can be canted at an angle as shown in FIG. 1, or it can be not canted at an angle (i.e. level or flat).

[0023] Substrate 35 can be, for example, transferred into and out of processing chamber 10 through a slot valve (not shown) and chamber feed-through (not shown) via robotic substrate transfer system where it is received by substrate lift pins (not shown) housed within substrate holder 30 and mechanically translated by devices housed therein. Once substrate 35 is received from the substrate transfer system, it is lowered to an upper surface of substrate holder 30.

[0024] Substrate 35 can be, for example, affixed to the substrate holder 30 via an electrostatic clamping system. Furthermore, substrate holder 30 can, for example, further include a cooling system including a re-circulating coolant flow that receives heat from substrate holder 30 and transfers heat to a heat exchanger system (not shown), or when heating, transfers heat from the heat exchanger system to substrate holder 30. Moreover, gas can, for example, be delivered to the back-side of substrate 35 via a backside gas system to improve the gas-gap thermal conductance between substrate 35 and

substrate holder 30. Such a system can be utilized when temperature control of the substrate is required at elevated or reduced temperatures. In other embodiments, heating elements, such as resistive heating elements, or thermo-electric heaters/coolers can be included.

[0025] In the illustrated embodiment, shown in FIG. 1, substrate holder 30 can comprise an electrode (not shown) through which RF power is coupled to the processing plasma in process space 12. For example, substrate holder 30 can be electrically biased at a RF voltage via the transmission of RF power from a RF generator (not shown) through an impedance match network (not shown) to substrate holder 30. The RF bias can serve to heat electrons to form and maintain plasma. In this configuration, the system can operate as a reactive ion etch (RIE) reactor, wherein the chamber and upper gas injection electrode serve as ground surfaces. A typical frequency for the RF bias can range from 1 MHz to 100 MHz, for example, 13.56 MHz. RF systems for plasma processing are well known to those skilled in the art.

[0026] Alternately, the processing plasma formed in process space 12 can be formed using a parallel-plate, capacitively coupled plasma (CCP) source, an inductively coupled plasma (ICP) source, transformer coupled plasma (TCP) source, any combination thereof, and with and without DC magnet systems. Alternately, the processing plasma in process space 12 can be formed using electron cyclotron resonance (ECR). In yet another embodiment, the processing plasma in process space 12 is formed from the launching of a Helicon wave. In yet another embodiment, the processing plasma in process space 12 is formed from a propagating surface wave.

[0027] In FIG. 2, an expanded cross-sectional view of a portion of substrate holder 30 is shown. The substrate holder 30 is configured to support substrate 35, and it is further configured to clamp substrate 35 to the substrate holder 30 using an electrostatic clamp (ESC) coupled to substrate holder 30. Substrate holder 30 can further comprise a coating layer 32. Additionally, substrate holder 30 comprises a step 34 to capture focus ring 60, wherein focus ring 60 is coupled to substrate holder 30 in order to affect the processing at the periphery of substrate 35. As illustrated in FIG. 2, substrate 35 comprises a peripheral edge 36 having a radius that extends beyond an inner radius of the step 34 in substrate holder 30 such that a backside surface

38 of substrate 35 becomes exposed. Moreover, focus ring 60 can comprise an inner lip 62 underlying the peripheral edge 36 of substrate 35, and forming a clearance space 70 there between. For example, the vertical dimension between the underside of substrate 35 and an upper surface of focus ring 60 can typically be 0.5 mm. Additionally, for example, the lateral dimension between the edge of substrate 35 and the focus ring 60 can typically be 2 mm. When processing substrate 35 and, more particularly, when etching features within a material film on substrate 35, the presence of the clearance space 70 can permit the deposition of process residue on the backside surface 38 of substrate 35. For example, these deposits are known to occur when etching low-k dielectric films such as SiLK (silicon low-k) commercially available from Dow Chemical.

[0028] FIG. 3 presents an expanded cross-sectional view of a portion of a substrate holder 130. The substrate holder 130 is configured to support a substrate 135, and it is further configured to clamp substrate 135 to the substrate holder 130 using an electrostatic clamp (ESC) coupled to substrate holder 130. The substrate holder 130 can further comprise a coating layer 132. Furthermore, as depicted in FIG. 3, substrate holder 130 comprises a focus ring 160 coupled to the substrate holder 130 and configured to surround substrate 135 wherein a clearance space 170 between substrate 135 and focus ring 160 is formed, and a secondary focus ring 180 coupled to focus ring 160 and configured to occupy at least a portion of clearance space 170. Substrate holder 130 comprises a step 134 having an inner radial step surface 137 and a step receiving surface 139 configured to mate with focus ring 160. Focus ring 160 comprises a lip 162 having a lip receiving surface 164 configured to receive secondary focus ring 180. The lip receiving surface 164 can extend between an inner radial lip surface 166 and an outer radial lip surface 168. Furthermore, inner radial lip surface 166 can, for example, mate with inner radial step surface 137 of substrate holder 130 as shown in FIG. 3.

[0029] As illustrated in FIG. 3, substrate 135 comprises a peripheral edge 136 having a radius that extends beyond the radius of inner radial step surface 137 in step 134 of substrate holder 130 such that a backside surface 138 of substrate 135 becomes exposed. Secondary focus ring 180 comprises an upper surface 182, a lower surface 184, an inner radial edge surface 186,

and an outer radial edge surface 188. At least a portion of upper surface 182 of secondary focus ring 180 mates with backside surface 138 of substrate 135, and at least a portion of lower surface 184 mates with lip receiving surface 164. The outer radial edge need not extend to the outer radial lip surface 168 but need only extend past the peripheral edge 136.

[0030] Secondary focus ring 180 is formed from a compliant material. For example, the compliant material comprises at least one of silicone rubber, polyimide (such as Vespel), and Teflon. Alternately, secondary focus ring 180 is formed from a rigid material, wherein focus ring 160 and secondary focus ring 180 are fabricated and assembled with substrate holder 130 with sufficient tolerances to avoid fracture of substrate 135 during clamping. For example, the rigid material comprises at least one of silicon, silicon carbide, silicon nitride, silicon dioxide, and carbon. Alternately, secondary focus ring 180 is formed from silicon having a resistivity equal to or less than $20\ \Omega\text{-cm}$. Alternately, secondary focus ring 180 is formed from silicon having a resistivity equal to or less than $2\ \Omega\text{-cm}$. Alternately, secondary focus ring 180 is formed from silicon having a resistivity equal to or less than $0.020\ \Omega\text{-cm}$.

[0031] In another embodiment, FIG. 4 presents an expanded cross-sectional view of a portion of a substrate holder 230. The substrate holder 230 is configured to support a substrate 235, and it is further configured to clamp substrate 235 to the substrate holder 230 using an electrostatic clamp (ESC) coupled to substrate holder 230. The substrate holder 230 can further comprise a coating layer 232. Furthermore, as depicted in FIG. 4, substrate holder 230 comprises a focus ring 260 coupled to the substrate holder 230 and configured to surround substrate 235 wherein a clearance space 270 between substrate 235 and focus ring 260 is formed, and a secondary focus ring 280 coupled to focus ring 260 and configured to occupy at least a portion of clearance space 270. Substrate holder 230 comprises a step 234 having an inner radial step surface 237 and a step receiving surface 239 configured to mate with focus ring 260. Focus ring 260 comprises a lip 262 having a receiving surface 264 configured to receive secondary focus ring 280. The receiving surface 264 can extend between an inner radial lip surface 266 and an outer radial lip surface 268. Furthermore, inner radial lip surface 266 can,

for example, mate with inner radial step surface 237 of substrate holder 230 as shown in FIG. 4.

[0032] As illustrated in FIG. 4, substrate 235 comprises a peripheral edge 236 having a radius that extends beyond the radius of inner radial step surface 237 in step 234 of substrate holder 230 such that a backside surface 238 of substrate 235 becomes exposed. Secondary focus ring 280 comprises an upper surface 282, a lower surface 284, an inner radial edge surface 286, and an outer radial edge surface 288. At least a portion of lower surface 284 mates with step receiving surface 264.

[0033] Secondary focus ring 280 is formed from a rigid material. For example, the rigid material comprises at least one of silicon, silicon carbide, silicon nitride, silicon dioxide, and carbon. Alternately, secondary focus ring 280 is formed from a compliant material. For example, the compliant material comprises at least one of silicone rubber, polyimide (such as Vespel), and Teflon. Alternately, secondary focus ring 280 is formed from silicon having a resistivity equal to or less than $20\ \Omega\text{-cm}$. Alternately, secondary focus ring 280 is formed from silicon having a resistivity equal to or less than $2\ \Omega\text{-cm}$. Alternately, secondary focus ring 280 is formed from silicon having a resistivity equal to or less than $0.020\ \Omega\text{-cm}$.

[0034] In another embodiment, FIG. 5 presents an expanded cross-sectional view of a portion of a substrate holder 330. The substrate holder 330 is configured to support a substrate 335, and it is further configured to clamp substrate 335 to the substrate holder 330 using an electrostatic clamp (ESC) coupled to substrate holder 330. The substrate holder 330 can further comprise a coating layer 332. Furthermore, as depicted in FIG. 5, substrate holder 330 comprises a focus ring 360 coupled to the substrate holder 330 and configured to surround substrate 335 wherein a clearance space 370 between substrate 335 and focus ring 360 is formed, and a secondary focus ring 380 coupled to focus ring 360 and configured to occupy at least a portion of clearance space 370. Substrate holder 330 comprises a step 334 having an inner radial step surface 337 and a step receiving surface 339 configured to mate with focus ring 360. Focus ring 360 comprises a lip 362 having a receiving surface 364 configured to receive secondary focus ring 380. The

receiving surface 364 can extend between an inner radial lip surface 366 and an outer radial lip surface 368. Furthermore, inner radial lip surface 366 can, for example, mate with inner radial step surface 337 of substrate holder 330 as shown in FIG. 5.

[0035] As illustrated in FIG. 5, substrate 335 comprises a peripheral edge 336 having a radius that extends beyond the radius of inner radial step surface 337 in step 334 of substrate holder 330 such that a backside surface 338 of substrate 335 becomes exposed. Secondary focus ring 380 comprises a first upper surface 381, a second upper surface 382 coupled to the first upper surface 381 via an upper radial surface 383, a lower surface 384, an inner radial edge surface 386, and an outer radial edge surface 388. At least a portion of lower surface 384 mates with step receiving surface 364.

[0036] Secondary focus ring 380 is formed from a rigid material. For example, the rigid material comprises at least one of silicon, silicon carbide, silicon nitride, silicon dioxide, and carbon. Alternately, secondary focus ring 380 is formed from a compliant material. For example, the compliant material comprises at least one of silicone rubber, polyimide (such as Vespel), and Teflon. Alternately, secondary focus ring 380 is formed from silicon having a resistivity equal to or less than $20\ \Omega\text{-cm}$. Alternately, secondary focus ring 380 is formed from silicon having a resistivity equal to or less than $2\ \Omega\text{-cm}$. Alternately, secondary focus ring 380 is formed from silicon having a resistivity equal to or less than $0.020\ \Omega\text{-cm}$.

[0037] In another embodiment, FIG. 6 presents an expanded cross-sectional view of a portion of a substrate holder 430. The substrate holder 430 is configured to support a substrate 435, and it is further configured to clamp substrate 435 to the substrate holder 430 using an electrostatic clamp (ESC) coupled to substrate holder 430. The substrate holder 430 can further comprise a coating layer 432. Furthermore, as depicted in FIG. 6, substrate holder 430 comprises a focus ring 460 coupled to the substrate holder 430 and configured to surround substrate 435 wherein a clearance space 470 between substrate 435 and focus ring 460 is formed, and a secondary focus ring 480 coupled to focus ring 460 and configured to be interposed between the focus ring 460 and the substrate holder 430. Substrate holder 430

comprises a step 434 having an inner radial step surface 437 and a step receiving surface 439 configured to mate with focus ring 460. Focus ring 460 comprises a lip 462 having a receiving surface 464. The receiving surface 464 can extend between an inner radial lip surface 466 and an outer radial lip surface 468, wherein the inner radial lip surface 466 mates with the secondary focus ring 480 as shown in FIG. 6.

[0038] As illustrated in FIG. 6, substrate 435 comprises a peripheral edge 436 having a radius that extends beyond the radius of inner radial step surface 437 in step 434 of substrate holder 430 such that a backside surface 438 of substrate 435 becomes exposed. Secondary focus ring 480 comprises an upper surface 482, a lower surface 484, an inner radial edge surface 486, and an outer radial edge surface 488. At least a portion of outer radial edge surface 488 mates with inner radial step surface 466, and at least a portion of lower surface 484 mates with step receiving surface 439. The secondary focus ring 480 may optionally include a protective portion 492 interposed between the focus ring 460 and the substrate 435.

[0039] Secondary focus ring 480 is formed from a rigid material. For example, the rigid material comprises at least one of silicon, silicon carbide, silicon nitride, silicon dioxide, and carbon. Alternately, secondary focus ring 480 is formed from a compliant material. For example, the compliant material comprises at least one of silicone rubber, polyimide (such as Vespel), and Teflon. Alternately, secondary focus ring 480 is formed from silicon having a resistivity equal to or less than $20\ \Omega\text{-cm}$. Alternately, secondary focus ring 480 is formed from silicon having a resistivity equal to or less than $2\ \Omega\text{-cm}$. Alternately, secondary focus ring 480 is formed from silicon having a resistivity equal to or less than $0.020\ \Omega\text{-cm}$.

[0040] In another embodiment, FIG. 7 presents an expanded cross-sectional view of a portion of a substrate holder 530. The substrate holder 530 is configured to support a substrate 535, and it is further configured to clamp substrate 535 to the substrate holder 530 using an electrostatic clamp (ESC) coupled to substrate holder 530. The substrate holder 530 can further comprise a coating layer 532. Furthermore, as depicted in FIG. 7, substrate holder 530 comprises a focus ring 560 coupled to the substrate holder 530

and configured to surround substrate 535 wherein a clearance space 570 between substrate 535 and focus ring 560 is formed, and a secondary focus ring 580 coupled to focus ring 560, configured to be interposed between the focus ring 560 and the substrate holder 530, and configured to affect a reduced clearance space 570' between substrate 535 and an upper surface thereof. Substrate holder 530 comprises a step 534 having an inner radial step surface 537 and a step receiving surface 539 configured to receive focus ring 560 and secondary focus ring 580. Focus ring 560 comprises an inner radial mating surface 568 configured to mate with secondary focus ring 580.

[0041] As illustrated in FIG. 7, substrate 535 comprises a peripheral edge 536 having a radius that extends beyond the radius of inner radial step surface 537 in step 534 of substrate holder 530 such that a backside surface 538 of substrate 535 becomes exposed. Secondary focus ring 580 comprises an upper surface 582, a lower surface 584, an inner radial edge surface 586, and an outer radial edge surface 588. At least a portion of outer radial edge surface 588 mates with inner radial mating surface 568, and at least a portion of lower surface 584 mates with step receiving surface 539.

[0042] Secondary focus ring 580 is formed from a rigid material. For example, the rigid material comprises at least one of silicon, silicon carbide, silicon nitride, silicon dioxide, and carbon. Alternately, secondary focus ring 580 is formed from a compliant material. For example, the compliant material comprises at least one of silicone rubber, polyimide (such as Vespel), and Teflon. Alternately, secondary focus ring 580 is formed from silicon having a resistivity equal to or less than $20\ \Omega\text{-cm}$. Alternately, secondary focus ring 580 is formed from silicon having a resistivity equal to or less than $2\ \Omega\text{-cm}$. Alternately, secondary focus ring 580 is formed from silicon having a resistivity equal to or less than $0.020\ \Omega\text{-cm}$.

[0043] In another embodiment, FIG. 8 presents an expanded cross-sectional view of a portion of a substrate holder 630. The substrate holder 630 is configured to support a substrate 635, and it is further configured to clamp substrate 635 to the substrate holder 630 using an electrostatic clamp (ESC) coupled to substrate holder 630. The substrate holder 630 can further comprise a coating layer 632. Furthermore, as depicted in FIG. 8, substrate

holder 630 comprises a focus ring 660 coupled to the substrate holder 630 and configured to surround substrate 635 wherein a clearance space 670 between substrate 635 and focus ring 660 is formed, and a secondary focus ring 680 coupled to focus ring 660, configured to be interposed between the focus ring 660 and the substrate holder 630, and configured to affect a reduced clearance space 670' between substrate 635 and an upper surface thereof. Substrate holder 630 comprises a step 634 having an inner radial step surface 637 and a step receiving surface 639 configured to receive focus ring 660 and secondary focus ring 680. Focus ring 660 comprises an inner radial mating surface 668 configured to mate with secondary focus ring 680.

[0044] As illustrated in FIG. 8, substrate 635 comprises a peripheral edge 636 having a radius that extends beyond the radius of inner radial step surface 637 in step 634 of substrate holder 630 such that a backside surface 638 of substrate 635 becomes exposed. Secondary focus ring 680 comprises a first upper surface 681, a second upper surface 682 coupled to the first upper surface 681 via an upper radial surface 683, a lower surface 684, an inner radial edge surface 686, and an outer radial edge surface 688. At least a portion of outer radial edge surface 688 mates with inner radial mating surface 668, and at least a portion of lower surface 684 mates with step receiving surface 639.

[0045] Secondary focus ring 680 is formed from a rigid material. For example, the rigid material comprises at least one of silicon, silicon carbide, silicon nitride, silicon dioxide, and carbon. Alternately, secondary focus ring 680 is formed from a compliant material. For example, the compliant material comprises at least one of silicone rubber, polyimide (such as Vespel), and Teflon. Alternately, secondary focus ring 680 is formed from silicon having a resistivity equal to or less than $20\ \Omega\text{-cm}$. Alternately, secondary focus ring 680 is formed from silicon having a resistivity equal to or less than $2\ \Omega\text{-cm}$. Alternately, secondary focus ring 680 is formed from silicon having a resistivity equal to or less than $0.020\ \Omega\text{-cm}$.

[0046] In FIGs. 3 through 8, focus rings 160, 260, 360, 460, 560, and 660, and secondary focus rings 180, 280, 380, 480, 580, and 680 can, for example, be fabricated using at least one of machining, polishing, and

grinding. For example, each focus ring and secondary focus ring described above can be machined according to specifications set forth on a mechanical drawing, using conventional techniques including a mill, etc. The techniques for machining a component using, for example, a mill, are well known to those skilled in the art of machining such materials. Focus rings may also include gas channels for providing gas into a clearance space (e.g., 270) to further remove particles that might otherwise accumulate on the backside of the substrate.

[0047] In an example, the thickness of the substrate backside deposition was measured for several focus ring assembly configurations, each focus ring assembly comprising a focus ring and a secondary focus ring. A multi-step process recipe was executed for both bare silicon substrates and blanket photoresist (PR) on silicon substrates with several of the above-described configurations. The multi-step process recipe comprised the following steps: Chamber pressure = 60 mTorr, Upper electrode RF power = 2200 W, Lower electrode RF power = 1800 W, Process gas flow rate $\text{CF}_4/\text{O}_2/\text{Ar}$ = 150/20/800 sccm, a 30 mm electrode spacing between the lower surface of an upper electrode and the upper surface of the substrate on the substrate holder, Lower electrode temperature (e.g., substrate holder) = 0C, Upper electrode temperature = 30C, Chamber wall temperature = 50C, Backside helium pressure Center/Edge = 10/35 Torr, and an etch time of 50 seconds; Chamber pressure = 300 mTorr, Upper electrode RF power = 1500 W, Lower electrode RF power = 1200 W, Process gas flow rate N_2/H_2 = 500/100 sccm, a 55 mm electrode spacing between the lower surface of an upper electrode and the upper surface of the substrate on the substrate holder, Lower electrode temperature (e.g., substrate holder) = 0C, Upper electrode temperature = 30C, Chamber wall temperature = 50C, Backside helium pressure Center/Edge = 10/35 Torr, and an etch time of 65 seconds; Chamber pressure = 50 mTorr, Upper electrode RF power = 1000 W, Lower electrode RF power = 100 W, Process gas flow rate $\text{CH}_2\text{F}_2/\text{O}_2/\text{Ar}$ = 20/40/200 sccm, a 55 mm electrode spacing between the lower surface of an upper electrode and the upper surface of the substrate on the substrate holder, Lower electrode temperature (e.g., substrate holder) = 0C, Upper electrode temperature = 30C, Chamber wall temperature = 50C, Backside helium

pressure Center/Edge = 10/35 Torr, and an etch time of 17 seconds;
 Chamber pressure = 50 mTorr, Upper electrode RF power = 1000 W, Lower electrode RF power = 100 W, Process gas flow rate $\text{CH}_2\text{F}_2/\text{CF}_4/\text{O}_2/\text{Ar}$ = 20/20/20/200 sccm, a 55 mm electrode spacing between the lower surface of an upper electrode and the upper surface of the substrate on the substrate holder, Lower electrode temperature (e.g., substrate holder) = 0C, Upper electrode temperature = 30C, Chamber wall temperature = 50C, Backside helium pressure Center/Edge = 10/35 Torr, and an etch time of 20 seconds;
 Chamber pressure = 800 mTorr, Upper electrode RF power = 1500 W, Lower electrode RF power = 1200 W, Process gas flow rate N_2/H_2 = 300/300 sccm, a 55 mm electrode spacing between the lower surface of an upper electrode and the upper surface of the substrate on the substrate holder, Lower electrode temperature (e.g., substrate holder) = 0C, Upper electrode temperature = 30C, Chamber wall temperature = 50C, Backside helium pressure Center/Edge = 10/35 Torr, and an etch time of 29 seconds;
 Chamber pressure = 50 mTorr, Upper electrode RF power = 1000 W, Lower electrode RF power = 100 W, Process gas flow rate $\text{CH}_2\text{F}_2/\text{O}_2/\text{Ar}$ = 25/20/200 sccm, a 55 mm electrode spacing between the lower surface of an upper electrode and the upper surface of the substrate on the substrate holder, Lower electrode temperature (e.g., substrate holder) = 0C, Upper electrode temperature = 30C, Chamber wall temperature = 50C, Backside helium pressure Center/Edge = 10/35 Torr, and an etch time of 15 seconds; and
 Chamber pressure = 800 mTorr, Upper electrode RF power = 1500 W, Lower electrode RF power = 1200 W, Process gas flow rate N_2/H_2 = 300/300 sccm, a 55 mm electrode spacing between the lower surface of an upper electrode and the upper surface of the substrate on the substrate holder, Lower electrode temperature (e.g., substrate holder) = 0C, Upper electrode temperature = 30C, Chamber wall temperature = 50C, Backside helium pressure Center/Edge = 10/35 Torr, and an etch time of 7 seconds.

[0048] Table I presents the measured thickness (nm) of the substrate backside deposition for each configuration described and the two different types of substrates.

| TEST EVALUATION CASE | REFERENCE | Polymer (Bare-Si) | Polymer (PR) |
|-------------------------------|-----------|-------------------|----------------|
| | | THICKNESS (nm) | THICKNESS (nm) |
| STANDARD | FIG. 2 | 35-55 | 77 |
| EVAL01 | FIG. 3 | 0 | 0 |
| EVAL02 | FIG. 4 | <10 / 34 | 59 |
| EVAL03 | FIG. 5 | 36 | NA |
| EVAL04 | FIG. 6 | 36 | NA |
| EVAL05 | FIG. 7 | 34 | NA |
| EVAL06 | FIG. 8 | 17 | NA |
| STANDARD (0.005 Ω -cm) | NA | 29 | NA |
| STANDARD (1.500 Ω -cm) | NA | 30 | NA |
| STANDARD (18.00 Ω -cm) | NA | 40 | NA |

TABLE I.

[0049] Referring now to FIG. 9, a method of reducing substrate backside deposition during processing is described. The method is presented in flow chart 800 beginning with step 810, wherein a focus ring assembly comprising a focus ring and a secondary focus ring such as the ones described in FIGs. 3 through 8 is installed in a processing system such as the one described in FIG. 1. The installation can take place during the initial installation of the processing system in a manufacturing environment or anytime thereafter by anyone skilled in the assembly and maintenance of such systems. For example, the focus ring and the secondary focus ring can be installed, thereby replacing a previously used focus ring and secondary focus ring, during maintenance intervals, such as for chamber cleaning, process kit replacement, etc. During installation, the focus ring and the secondary focus ring can be positioned upon an upper surface of the substrate holder. Alternately, the focus ring and/or secondary focus ring can be clamped to the substrate holder using either a mechanical clamping system, or an electrical clamping system as known to those skilled in the art of electrostatic clamping systems.

[0050] In step 820, a substrate is loaded into the processing system using techniques known to those skilled in the design of substrate transfer systems. In step 830, the substrate is processed in the processing system. The processing of the substrate can, for example, comprise material processing including either the addition of material to the substrate or the removal of material from the substrate.

[0051] Although only certain exemplary embodiments of this invention have been described in detail above, those skilled in the art will readily appreciate that many modifications are possible in the exemplary embodiments without materially departing from the novel teachings and advantages of this invention. Accordingly, all such modifications are intended to be included within the scope of this invention.

CLAIMS:

What is claimed is:

1. A focus ring assembly comprising:
a focus ring; and
a secondary focus ring coupled to said focus ring, wherein said focus ring is configured to couple to a substrate holder configured to support a substrate exposed to a process in a processing system, and said secondary focus ring is configured to reduce deposition of material from said process on a backside surface of said substrate.
2. The substrate holder as recited in claim 1, wherein said secondary focus ring comprises a compliant material.
3. The substrate holder as recited in claim 2, wherein said compliant material comprises at least one of silicone rubber, polyimide, and Teflon.
4. The substrate holder as recited in claim 1, wherein said secondary focus ring comprises a rigid material.
5. The substrate holder as recited in claim 4, wherein said rigid material comprises at least one of a ceramic material, silicon, silicon carbide, silicon nitride, silicon dioxide, carbon, sapphire, and alumina.
6. The substrate holder as recited in claim 1, wherein said secondary focus ring comprises silicon having a resistivity less than or equal to 1 Ω -cm.
7. The substrate holder as recited in claim 1, wherein a clearance space is formed between said substrate and said focus ring, and said clearance space exposes at least a portion of said backside surface on said substrate and said secondary focus ring reduces said clearance space.

8. The substrate holder as recited in claim 7, wherein said secondary focus ring reduces exposure of said backside surface.

9. The substrate holder as recited in claim 1, wherein a portion of said backside surface on said substrate is exposed and wherein said secondary focus ring reduces said exposure of said backside surface.

10. The substrate holder as recited in claim 1, wherein said secondary focus ring makes contact with said substrate and makes contact with said focus ring.

11. A method of using a focus ring assembly for surrounding a substrate upon a substrate holder in a processing system comprising:

installing said focus ring assembly in said processing system, wherein said focus ring assembly comprises a focus ring coupled to said substrate holder, and a secondary focus ring coupled to said focus ring and configured to reduce deposition of material from said process on a backside surface of said substrate;

loading said substrate into said processing system; and
processing said substrate.

12. The method as recited in claim 11, wherein said secondary focus ring comprises a compliant material.

13. The method as recited in claim 12, wherein said compliant material comprises at least one of silicone rubber, polyimide, and Teflon.

14. The method as recited in claim 11, wherein said secondary focus ring comprises a rigid material.

15. The method as recited in claim 14, wherein said rigid material comprises at least one of a ceramic material, silicon, silicon carbide, silicon nitride, silicon dioxide, carbon, sapphire, and alumina.

16. The method as recited in claim 11, wherein said secondary focus ring comprises silicon having a resistivity less than or equal to 1 Ω -cm.

17. The method as recited in claim 11, wherein a clearance space is formed between said substrate and said focus ring, and said clearance space exposes at least a portion of said backside surface on said substrate and said secondary focus ring reduces said clearance space.

18. The method as recited in claim 17, wherein said secondary focus ring reduces exposure of said backside surface.

19. The method as recited in claim 11, wherein a portion of said backside surface on said substrate is exposed and wherein said secondary focus ring reduces said exposure of said backside surface.

20. The method as recited in claim 11, wherein said secondary focus ring makes contact with said substrate and makes contact with said focus ring.

**METHOD AND APPARATUS FOR REDUCING SUBSTRATE BACKSIDE
DEPOSITION DURING PROCESSING**

ABSTRACT OF DISCLOSURE

A focus ring assembly configured to be coupled to a substrate holder comprises a focus ring and a secondary focus ring coupled to the focus ring, wherein the secondary focus ring is configured to reduce the deposition of process residue on a backside surface of the substrate.

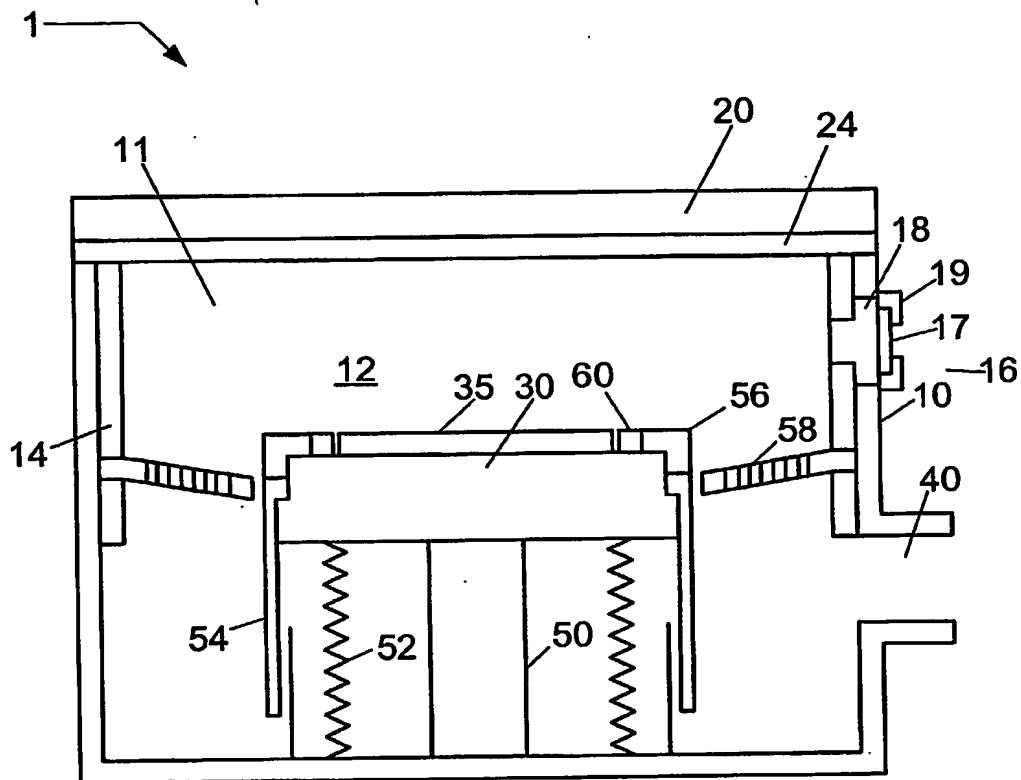
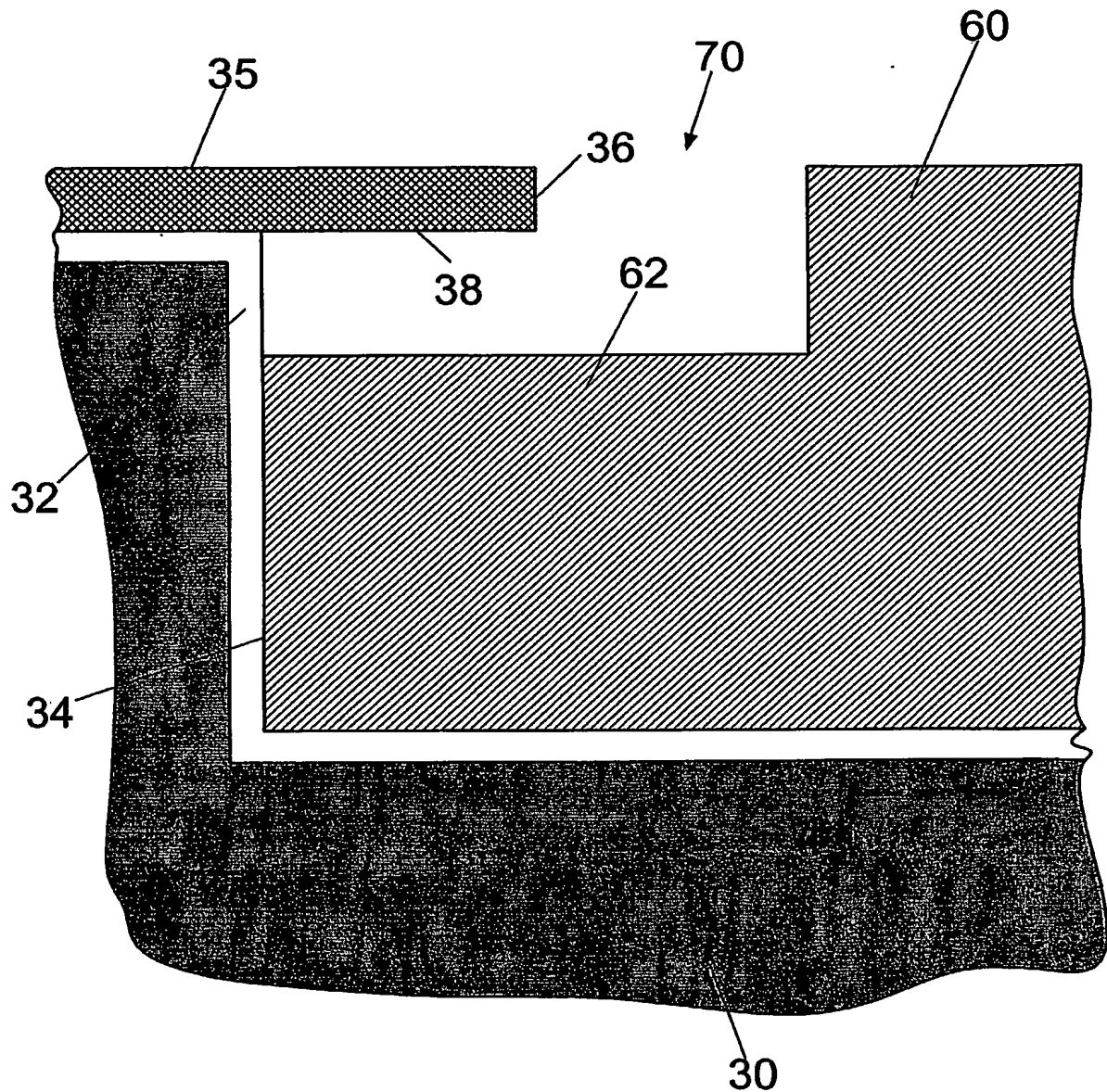
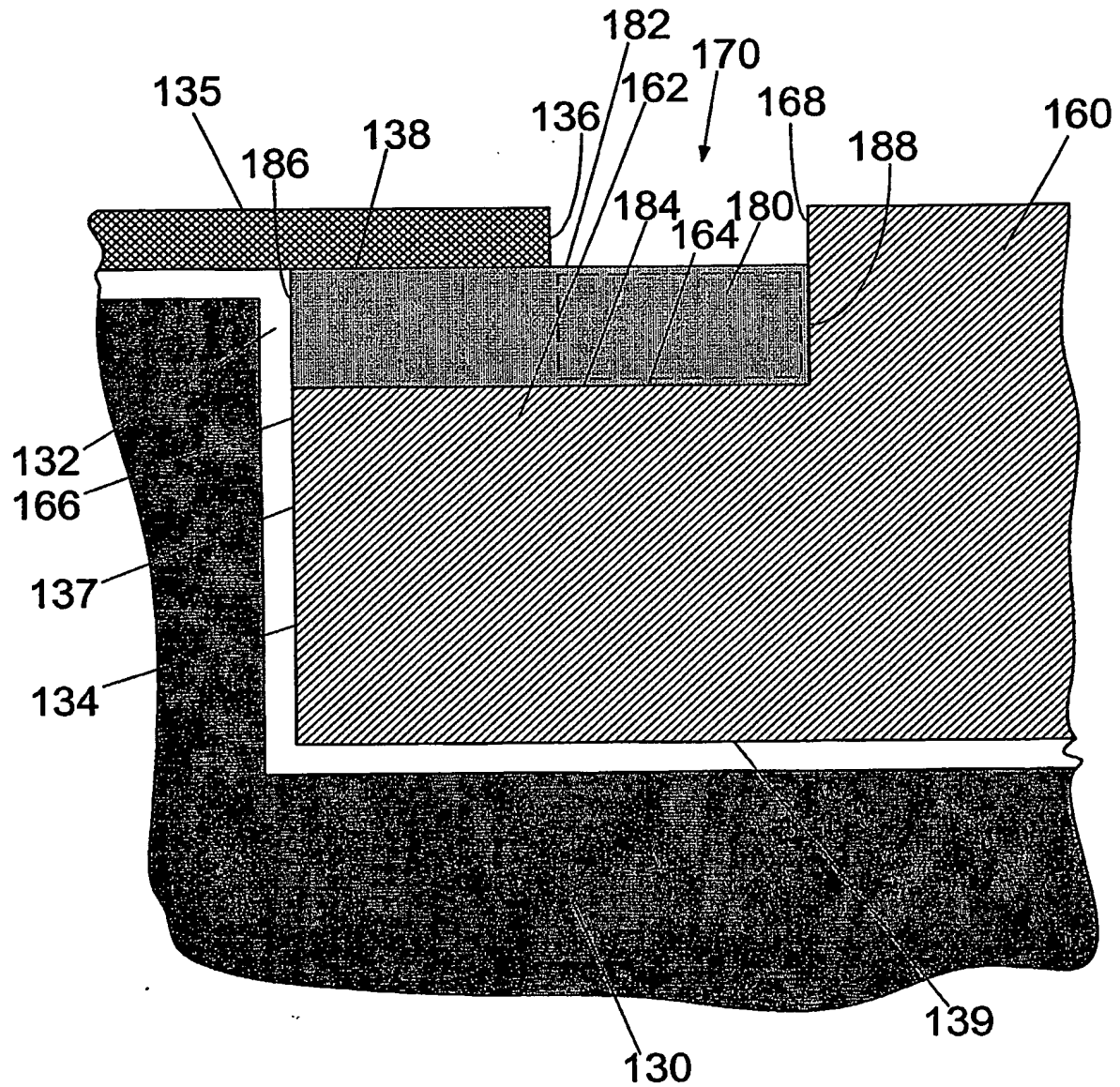


FIG. 1.



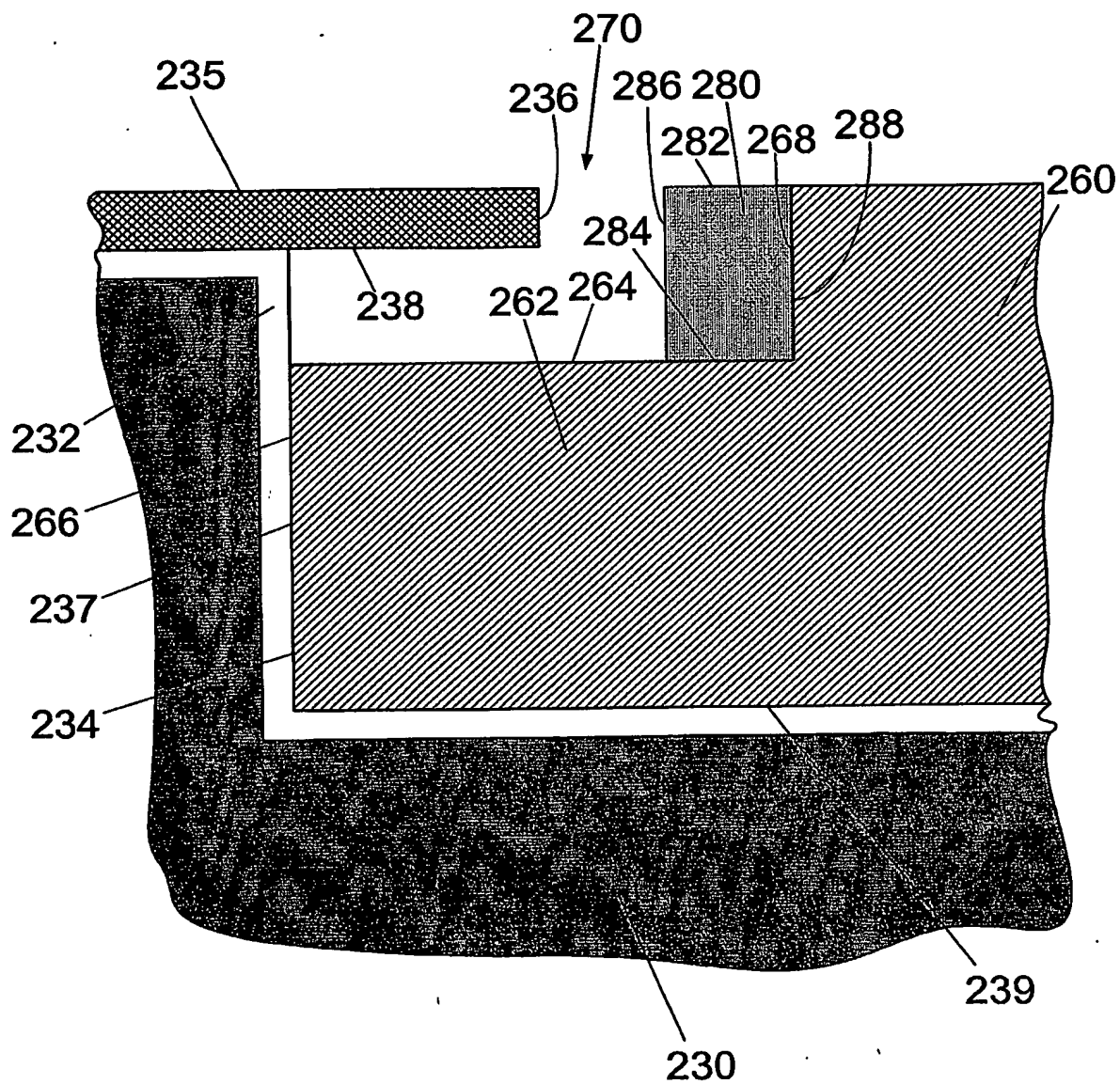
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FIG. 2.



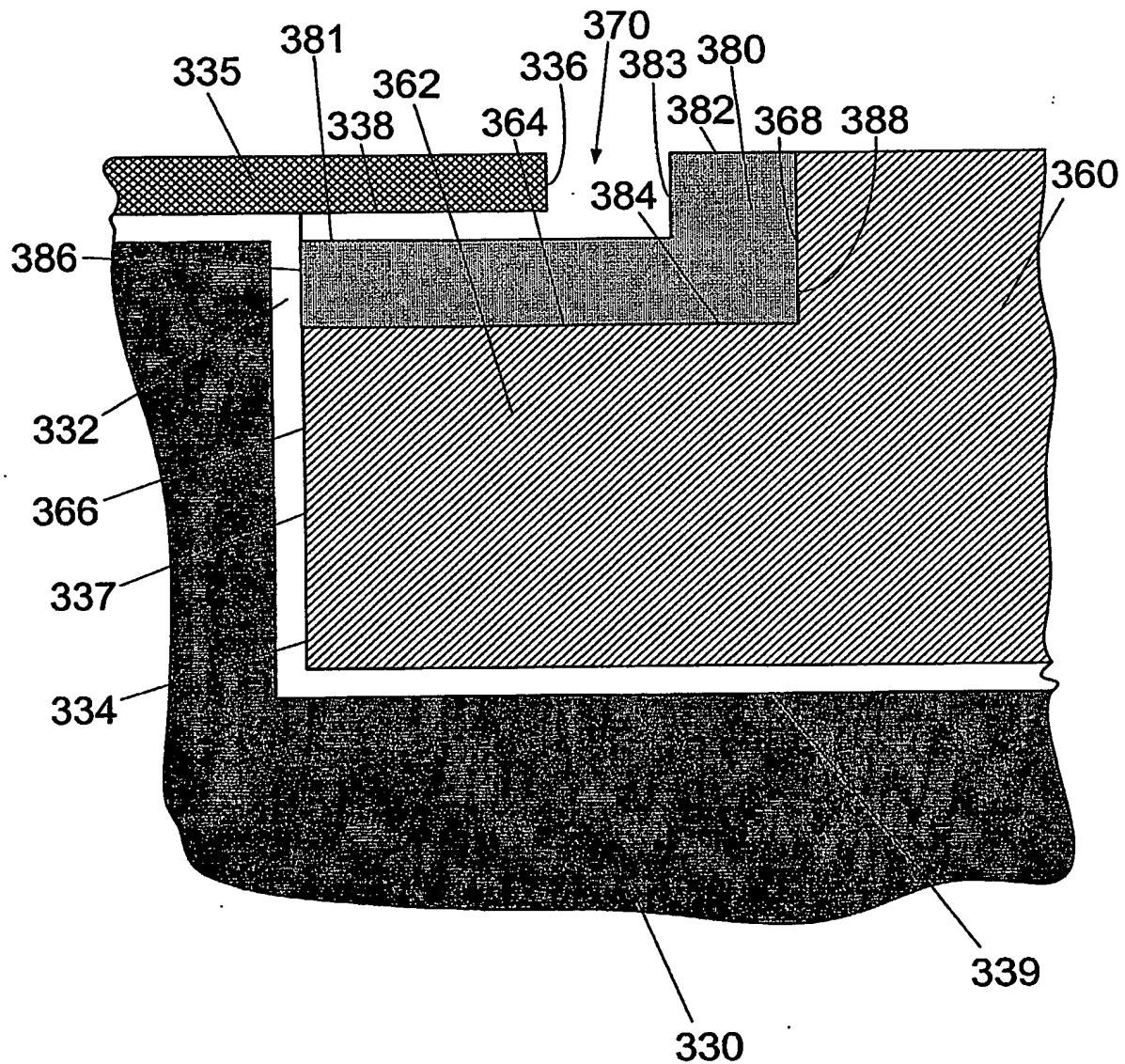
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FIG. 3.



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FIG. 4.

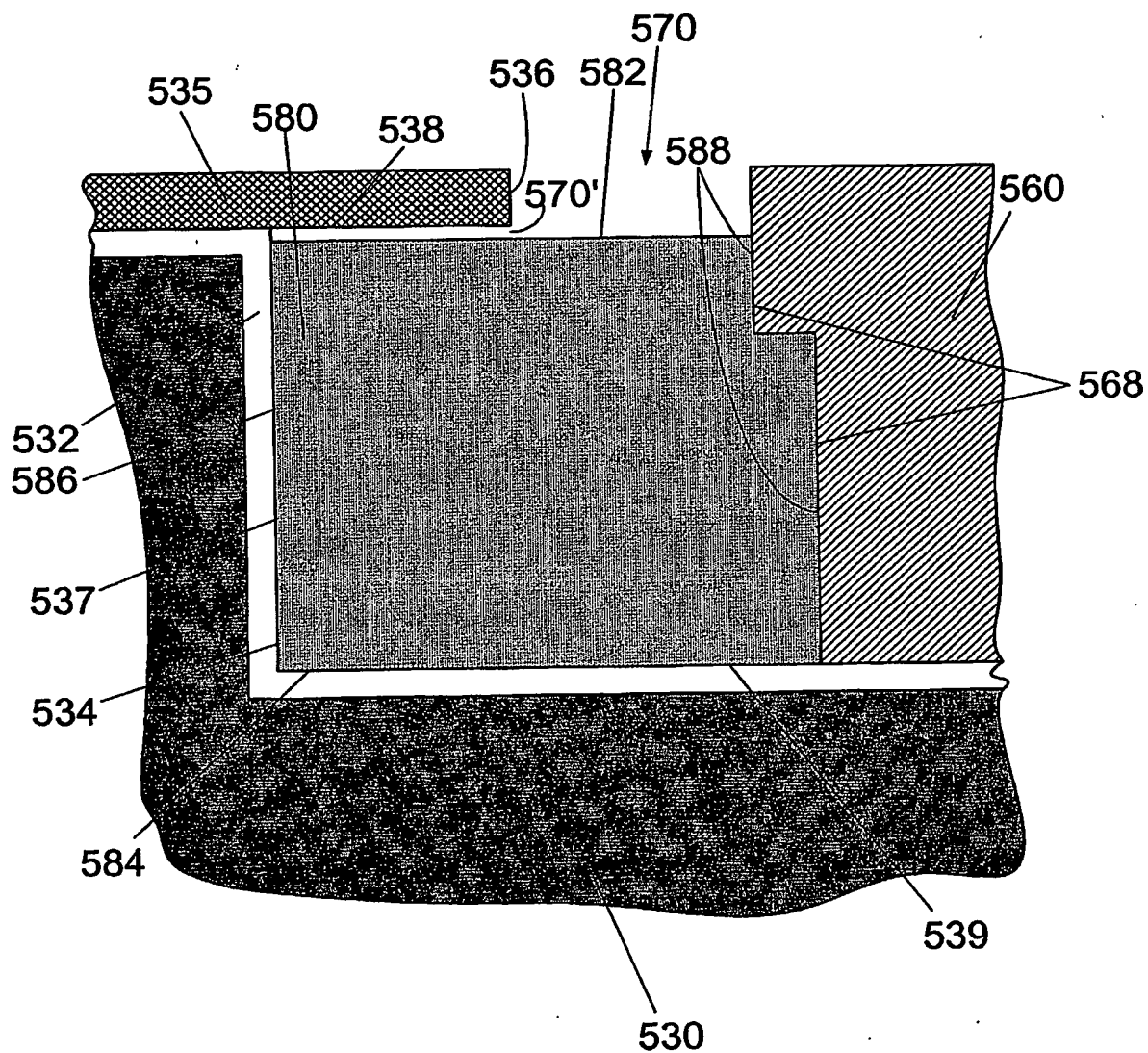


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FIG. 5.

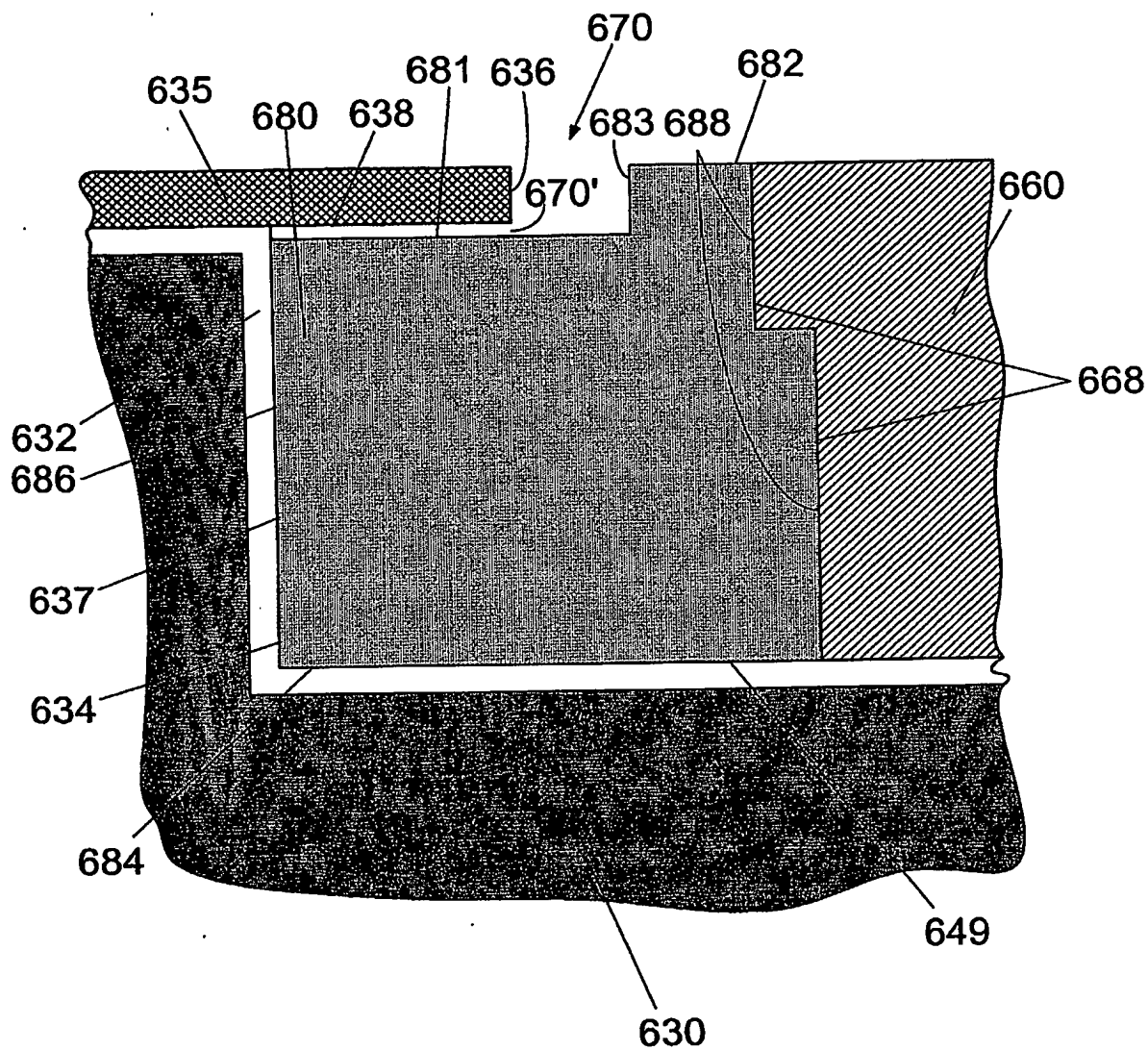


FIG. 6.



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FIG. 7.



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FIG. 8.

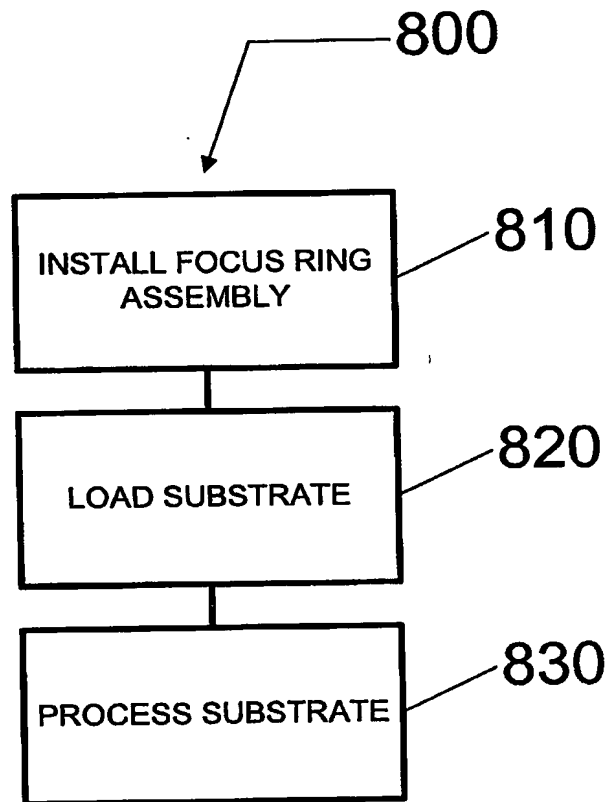


FIG. 9.